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FORM PTO-1390 (Modified) (REV 5-93)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTORNEY'S DOCKET NUMBER 040373-0317	
TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371					
		U.S. APPLICATION NO. (If known, see 37 CFR 1.51) Unassigned		10/088689	
INTERNATIONAL APPLICATION NO. PCT/JP00/06494	INTERNATIONAL FILING DATE 09/22/2000	PRIORITY DATE CLAIMED 09/24/1999			
TITLE OF INVENTION LOCAL OSCILLATION SIGNAL SUPPLY METHOD AND CIRCUIT					
APPLICANT(S) FOR DO/EO/US Yoshitaka KAWANABE					
Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:					
1.	<input checked="" type="checkbox"/>	This is a FIRST submission of items concerning a filing under 35 U.S.C. 371.			
2.	<input type="checkbox"/>	This is a SECOND or SUBSEQUENT submission of items concerning a filing under 35 U.S.C. 371.			
3.	<input type="checkbox"/>	This express request to begin national examination procedures (35 U.S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. 371(b) and PCT Articles 22 and 39(1).			
4.	<input checked="" type="checkbox"/>	A proper Demand for International Preliminary Examination was made by the 19 th month from the earliest claimed priority date.			
5.	<input checked="" type="checkbox"/>	A copy of the International Application as filed (35 U.S.C. 371(c)(2))			
	<input type="checkbox"/>	is transmitted herewith (required only if not transmitted by the International Bureau).			
	<input checked="" type="checkbox"/>	has been transmitted by the International Bureau.			
	<input type="checkbox"/>	is not required, as the application was filed in the United States Receiving Office (RO/US)			
6.	<input checked="" type="checkbox"/>	A translation of the International Application into English (35 U.S.C. 371(c)(2)).			
7.	<input checked="" type="checkbox"/>	Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3))			
	<input type="checkbox"/>	are transmitted herewith (required only if not transmitted by the International Bureau).			
	<input checked="" type="checkbox"/>	have been transmitted by the International Bureau.			
	<input type="checkbox"/>	have not been made; however, the time limit for making such amendments has NOT expired.			
	<input type="checkbox"/>	have not been made and will not be made.			
8.	<input checked="" type="checkbox"/>	A translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).			
9.	<input checked="" type="checkbox"/>	An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)).			
10.	<input type="checkbox"/>	A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)).			
11.	<input type="checkbox"/>	Applicant claims small entity status under 37 CFR 1.27.			
Items 12. to 17. below concern other document(s) or information included:					
12.	<input checked="" type="checkbox"/>	An Information Disclosure Statement under 37 CFR 1.97 and 1.98.			
13.	<input checked="" type="checkbox"/>	An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.			
14.	<input type="checkbox"/>	A FIRST preliminary amendment.			
	<input type="checkbox"/>	A SECOND or SUBSEQUENT preliminary amendment.			
15.	<input type="checkbox"/>	A substitute specification.			
16.	<input type="checkbox"/>	A change of power of attorney and/or address letter.			
17.	<input checked="" type="checkbox"/>	Other items or information: Application Data Sheet (3 pgs.), International Search Report (5 pgs.), Translation of Article 34 Amendments (05/02/2001)			

JC13 Rec'd PCT/PTO 21 MAR 2002

U.S. APPLICATION NO (if known, see 37 CFR 1.55) Unassigned 107088689		INTERNATIONAL APPLICATION NO PCT/JP00/06494		ATTORNEY'S DOCKET NUMBER 040373-0317	
18. <input checked="" type="checkbox"/> The following fees are submitted:				CALCULATIONS	
Basic National Fee (37 CFR 1.492(a)(1)-(5): Search Report has been prepared by the EPO or JPO \$890.00					
International preliminary examination fee paid to USPTO (37 CFR 1.482) \$710.00					
No international preliminary examination fee paid to USPTO (37 CFR 1.482) but international search fee paid to USPTO (37 CFR 1.445(a)(2)) \$740.00					
Neither international preliminary examination fee (37 CFR 1.482) nor International search fee (37 CFR 1.445(a)(2)) paid to USPTO \$1,040.00					
International preliminary examination fee paid to USPTO (37 CFR 1.482) and all claims satisfied provisions of PCT Article 33(2)-(4) \$100.00					
ENTER APPROPRIATE BASIC FEE AMOUNT =				\$890.00	
Surcharge of \$130.00 for furnishing the oath or declaration later than 20 Months from the earliest claimed priority date (37 CFR 1.492(e))					
Claims	Number Filed	Included in Basic Fee	Extra Claims	Rate	
Total Claims	4	- 20	= 0	x \$18.00	\$0.00
Independent Claims	4	- 3	= 1	x \$84.00	\$84.00
Multiple dependent claim(s) (if applicable)				\$280.00	
TOTAL OF ABOVE CALCULATIONS =				\$974.00	
Reduction by 1/2 for filing by small entity, if applicable.				\$0.00	
SUBTOTAL =				\$974.00	
Processing fee of \$130.00 for furnishing English translation later the 20 months from the earliest claimed priority date (37 CFR 1.492(f)).				+	
TOTAL NATIONAL FEE =				\$974.00	
Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31). \$40.00 per property +				\$40.00	
TOTAL FEES ENCLOSED =				\$1014.00	
				Amount to be: refunded \$	
				charged \$	
a. <input checked="" type="checkbox"/> A check in the amount of \$1014.00 to cover the above fees is enclosed.					
b. <input type="checkbox"/> Please charge my Deposit Account No. <u>19-0741</u> in the amount of \$0.00 to the above fees. A duplicate copy of this sheet is enclosed.					
c. <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. <u>19-0741</u> . A duplicate copy of this sheet is enclosed.					
NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.					
SEND ALL CORRESPONDENCE TO:					
Foley & Lardner Customer Number: 22428  22428 PATENT TRADEMARK OFFICE			SIGNATURE <u>David A. Blumenthal</u> NAME DAVID A. BLUMENTHAL <u>Reg. # 41,348</u> REGISTRATION NUMBER 26,257		

6/pvt/15

LOCAL OSCILLATION SIGNAL SUPPLY METHOD AND CIRCUIT

Background of the Invention

Field of the Invention:

5 The present invention relates to a method and a circuit for supplying local oscillation signals that are used in the demodulation and output by a digital signal processor of received signals of a plurality of receivers that are each connected to a respective antenna, and more particularly to a method and circuit for supplying local oscillation signals for accurately reproducing
10 the differences in propagation delay of signals that have been received at antennas.

Description of the Related Art:

Local oscillation signals are used in receivers for converting frequency. In a method and circuit of the prior art for supplying local oscillation signals,
15 an oscillator that generates a local oscillation signal is provided for each of a plurality of receivers that are each provided with a respective antenna, and local oscillation signals are thus independently supplied to each receiver.

In a configuration in which local oscillation signals are independently supplied to receivers in this way, however, the phases of these local
20 oscillation signals are not fixed, and such a configuration therefore cannot be applied in cases such as an adaptive array antenna system that requires the accurate detection of the phase component in the received signals of antennas.

The prior-art example shown in Fig. 1 is the configuration shown in
25 Japanese Patent Laid-open No. 224138/1998. Receivers 102-1 to 102-n each apply, as input to mixers 103-1 to 103-n that perform frequency

conversion: received signals that have been received from antennas 101-1 to 101-n that are provided for each of receivers 102-1 to 102-n; and a local oscillation signal from local oscillator 104 that is provided in common to receivers 102-1 to 102-n. The output of each of mixers 103-1 to 103-n is sent
 5 by way of A/D (analog/digital) converters 105-1 to 105-n to DSP (digital signal processor) 106.

When a local oscillation signal is received from a local oscillator as in the example shown in Fig. 1, however, the occurrence of phase error that arises from the time delays caused by differences in wiring length cannot be
 10 avoided.

As a countermeasure against the occurrence of phase error and the consequent disqualification for application in cases in which the accurate detection of the phase component is required, there exists a common synthesizer method in which oscillators are provided for each channel and
 15 local oscillation signals are distributed to each receiver, but the resulting large scale of the device is problematic.

Of the methods and circuits for supplying local oscillation signals of the above-described prior art, whether local oscillation signals are independently generated for the mixers of each respective receiver or a local
 20 oscillator is shared between a plurality of receivers as shown in Fig. 1, the lack of phase control for the input signals that are applied to the mixers results in the inability to avoid phase error of the local oscillation signals that are used in frequency conversion, and the differences in propagation delay of the received signals that are received from each of a plurality of receivers
 25 therefore cannot be accurately reproduced. The problem therefore exists that accurate control based on phase cannot be achieved. This inability prevents

frequency data that contain a frequency component that is to be a local
oscillation signal and sends these data as a common signal source to the
plurality of receivers by way of a single signal sequence that corresponds to
a wireless channel; and, at each of the receivers, a local oscillation signal
5 generator that generates a local oscillation signal in which phase and
amplitude are matched at all of the receivers based on the frequency data
that have been received from the frequency data generator.

By means of this construction, a local oscillation signal having phase
and amplitude that are matched with other receivers is generated at each of
10 the receivers, and as a result, differences in propagation delay at antennas
for a received signal that is received by way of each of a plurality of receivers
can be accurately reproduced.

As one specific method for the above-described generation of local
oscillation signals, the signal source that is supplied is a digital signal, this
15 digital signal is converted to an analog signal based on a clock signal that is
common to all of the receivers to generate the local oscillation signal,
whereby data that are synchronized in common and a signal can be
generated by digital processing.

In one example of a specific circuit, a frequency data generator
20 outputs frequency data by means of a digital signal, and the local oscillation
signal generators of the receivers include digital/analog converters that,
based on a clock signal that is common to all of the receivers, convert the
digital signal to an analog signal and output the result.

An increase in circuit scale can be avoided by means of this type of
25 construction.

In the local oscillation signal supply method, a plurality of the signal

sequences are provided each having different frequency data, and frequency data that are received from each of the plurality of the signal sequences are subjected to quadrature amplitude modulation to generate a local oscillation signal having a prescribed frequency. More specifically, a plurality of the

5 signal sequences supply shift data, which correspond to phase advance data for the frequency data, to all of the receivers; and in the receivers, shift data in which a desired frequency is obtained from each of the plurality of signal sequences are selected and signals converted, and the selected and converted shift data and the frequency data are subjected to quadrature

10 amplitude modulation to generate a local oscillation signal having a desired frequency.

In a local oscillation signal supply circuit, the local oscillation signal generator of a receiver is provided with a quadrature modulator that performs quadrature amplitude modulation of frequency data that are applied from

15 each of a plurality of signal sequences to generate a local oscillation signal having a desired frequency. More specifically, frequency data generators are provided with a plurality of signal sequences for outputting to all of the receivers shift data that correspond to phase advance data for the frequency data. The receivers are provided with: a selector/converter that selects shift

20 data, in which a desired frequency is obtained from the plurality of signal sequences, from the signal sequences and performs signal conversion; and a quadrature modulator that performs quadrature amplitude modulation of the selected and converted shift data and the frequency data to generate a local oscillation signal having a desired frequency.

25

Brief Description of the Drawings

Fig. 1 is a function block diagram showing an example of the prior art.

Fig. 2 is a function block diagram showing an embodiment of the present invention.

Fig. 3 is a function block diagram showing a specific example of a
5 portion of Fig. 2.

Fig. 4 is a function block diagram showing a specific example in which a function has been added to Fig. 2.

Fig. 5 is a function block diagram showing a specific example of a portion of Fig. 4.

10 Fig. 6 is a function block diagram showing a detailed specific example of Fig. 5.

Detailed Description of the Preferred Embodiments

We now refer to the accompanying figures to describe a working
15 example of the present invention.

Referring first to Fig. 2, which is a block diagram showing one construction of a working example of a local oscillation signal supply circuit according to the present invention, a local oscillation signal supply circuit is shown that is constituted by: n receivers 2-1 to 2-n that are respectively
20 provided with antennas 1-1 to 1-n; A/D (Analog/Digital) converters 7-1 to 7-n that are provided for each of receivers 2-1 to 2-n; and frequency data generator 6 and DSP (Digital Signal Processor) 8 that are provided in common for each of receivers 2-1 to 2-n.

Local oscillation signal generators 3-1 to 3-n, mixers 4-1 to 4-n, and
25 filters 5-1 to 5-n are provided for respective receivers 2-1 to 2n. Local oscillation signal generators 3-1 to 3-n receive as input frequency data that

contain a frequency component that is generated by frequency data generator 6.

This working example differs from the example of the prior art shown in Fig. 1 in that local oscillation signal generators 3-1 to 3-n inside receivers 2-1 to 2-n receive frequency data that contain a frequency component from frequency data generator 6 and generate a local oscillation signal that is synchronized with this frequency data.

The basic construction in this working example is next described. However, since DSP 8 for processing signals that have been outputted from receivers 2-1 to 2-n is not directly related to the present invention and has the same constitution as the prior art according to functions, a detailed description regarding this component is here omitted.

If the received frequency has wavelength λ , antennas 1-1 to 1-n are non-directional antennas arranged at intervals of $\lambda/4$ or more. Local oscillation signal generator 3-n of receiver 2-n that is connected to antenna 1-n generates a local oscillation signal that is synchronized to frequency data that are received from frequency data generator 6, whereby local oscillation signals having matched phase and amplitude are generated at all receivers 2-1 to 2-n.

Mixers 4-1 to 4-n are components represented by, for example, DBM (Double Balanced Mixers), and these components convert received signals that are applied as input from antennas 1-1 to 1-n to an IF (Intermediate Frequency) signal using the local oscillation signals that are generated at local oscillation signal generators 3-1 to 3-n. Filters 5-1 to 5-n can be represented by, for example, SAW (Surface Acoustic Wave) filters, and these filters suppress unwanted radiation that is emitted from mixers 4-1 to 4-n.

The output of each of mixers 4-1 to 4-n is outputted to DSP 8 by way of filters 5-1 to 5-n and A/D converters 7-1 to 7-n.

Frequency data generator 6 generates data that contain a frequency component and supplies these data to each of local oscillation signal

5 generators 3-1 to 3-n of receivers 2-1 to 2-n. A/D converters 7-1 to 7-n receive respective outputs of filters 5-1 to 5-n that are provided in respective receivers 2-1 to 2-n, convert these outputs to digital, and send the result to DSP 8. In the case of an adaptive array antenna system, DSP 8 detects the reception delay phase between demodulated signals that have been
10 demodulated from the received signals that have been frequency-converted at receivers 2-1 to 2-n.

Referring now to Fig. 2, the operation and functions of the circuit shown in Fig. 2 is explained.

Frequency conversion is carried out for each of the signals that have
15 been received at antennas 1-1 to 1-n at receivers 2-1 to 2-n, respectively. The local oscillation signals that are used when carrying out this frequency conversion are generated in local oscillation signal generators 3-1 to 3-n in receivers 2-1 to 2-n, respectively, based on frequency data that are supplied from frequency data generator 6. Frequency data generator 6 directly
20 generates a sine wave waveform of the local oscillation signals that are used in frequency conversion, and outputs the frequency data that are supplied to local oscillation signal generators 3-1 to 3-n.

The frequency data that are generated by frequency data generator 6 enable local oscillation signal generators 3-1 to 3-n to generate local
25 oscillation signals that can reproduce frequency error and phase error that are synchronized in common for each of receivers 2-1 to 2-n. The local

oscillation signals that are generated based on this frequency data can therefore fix the passing phase of signals among receivers 2-1 to 2-n. The phase differences between the received signals that have been frequency-converted at each of receivers 2-1 to 2-n are then detected by DSP 8.

5 Thus, in cases such as an adaptive array antenna system that necessitate the accurate detection of the phase component of received signals that have been received at antennas 1-1 to 1-n, the passing phase of signals among receivers 2-1 to 2-n can be fixed and the relative phase fluctuation between demodulated signals therefore indicates the reception
10 delay phase for each of antennas 1-1 to 1-n. In other words, this feature stabilizes the operation of an adaptive array antenna system.

 Fig. 3 is a block diagram showing the specific construction of a receiver in the working example shown in Fig. 2. Explanation is here presented with reference to both Fig. 2 and Fig. 3 regarding one case in
15 which local oscillation signals are generated. Antenna 1, mixer 4, filter 5, A/D converter 7, receiver 20, D/A converter 21, and frequency data generator 22 in Fig. 3 correspond to antennas 1-1 to 1-n, mixers 4-1 to 4-n, filters 5-1 to 5-n, A/D converters 7-1 to 7-n, receivers 2-1 to 2-n, local oscillation signal generators 3-1 to 3-n, and frequency data generator 6, respectively, of Fig. 2.
20 In other words, in the example shown in Fig. 3, D/A converter 21 has been adopted as local oscillation signal generator 3-1 to 3-n in Fig. 2.

 Although only one receiver 20 is shown in Fig. 3, frequency data generator 22 may supply frequency data constituted by digital signals to the plurality of receivers 20 that are actually provided. In addition, the signal
25 source of the clock signal that is supplied to the digital system is common to all constituent elements including the plurality of receivers 20, and phase

error therefore does not occur inside one local oscillation signal supply circuit.

D/A converter 21 of each receiver 20 receives data that are synchronized in common with other receivers by means of frequency data that are supplied from frequency data generator 22, whereby D/A converter
 5 21 generates a waveform. Accordingly, the phase error of local oscillation signals that are outputted by D/A converter 21 can be fixed. In other words, differences in the frequency error and phase error of local oscillation signals among the receivers are prevented in the present embodiment. As a result, the passing phase of demodulated signals among the receivers is fixed, and
 10 detection by DSP 8 of the phase differences of received signals that have been received from a plurality of receivers 20 enables the relative phase deviation between the demodulated signals to be reliably determined as the reception delay phase in antenna 1. This reliable determination stabilizes the operation of an adaptive array antenna system.

15 Fig. 4 is a block diagram showing the constitution of a working example in which the functions shown in Fig. 2 and Fig. 3 have been extended, and explanation is next presented regarding the working example shown in Fig. 4.

This working example is capable of realizing frequency conversion at
 20 intermediate frequencies that differ for each receiver. The present working example differs from the working example shown in Fig. 3 in that: quadrature modulators 31-1 to 31-n are provided in n receivers 30-1 to 30-n, respectively; and shift data generator 9, in addition to frequency data generator 6, generates a number of items of shift data that allows
 25 accommodation of all channels of the reception bandwidth and sends these items of shift data to all receivers 30-1 to 30-n.

Each of mixers 4-1 to 4-n and filters 5-1 to 5-n that make up receivers 30-1 to 30-n, respectively, have the same functions as the components shown in Fig. 2 and redundant explanation regarding these components is omitted.

5 Quadrature modulators 31-1 to 31-n that are provided in receivers 30-1 to 30-n, respectively, receive as input both a signal of basic frequency that is based on frequency data that are supplied from frequency data generator 6 and a signal that is the detuning frequency for the above-described basic frequency and that is shift data that are supplied from shift data generator 9,
10 and quadrature modulators 31-1 to 31-n then perform quadrature modulation of these signals to output local oscillation signals of differing frequencies.

Referring now to Fig. 5, which is a block diagram showing the actual construction of a receiver in the working example shown in Fig. 4, explanation is presented with reference to Fig. 4 and Fig. 5 regarding an
15 example of the generation of local oscillation signals for a case having four channels of local oscillation signals.

Antenna 1, mixer 4, filter 5, receiver 40, frequency data generator 22, and shift data generator 44 in Fig. 5 correspond to antennas 1-1 to 1-n, mixers 4-1 to 4-n, filters 5-1 to 5-n, receivers 30-1 to 30-n, frequency data
20 generator 6, and shift data generator 9 in Fig. 4; and D/A converters 21 and 42, selector/converter 41, and quadrature modulator 43 in Fig. 5 correspond to quadrature modulators 31-1 to 31-n in Fig. 4.

Frequency data generator 22 generates a digital signal that indicates basic frequency f_0 and outputs this digital signal to quadrature modulator 43
25 by way of D/A converter 21. A clock is used that is common to D/A converters 21 and 42 that are provided in each of receivers 40.

Shift data generator 44 outputs shift data f_1 to f_4 to selector/converter 41 that correspond to four types of detuning frequencies. Selector/converter 41 is constituted by a DSP, receives frequency designation signal S that is sent in from DSP 8, selects any of shift data f_1 to f_4 that correspond to the
5 detuning frequency that is received from shift data generator 44, generates a digital signal that indicates the detuning frequency that corresponds to the selected shift data, and outputs the generated digital signal to quadrature modulator 43 by way of D/A converter 42. In this case, the frequencies that are to be set as the local oscillation signals are 40 MHz, 45 MHz, 50 MHz,
10 and 55 MHz. In this case, the data that frequency data generator 22 sends is a digital signal that indicates that basic frequency f_0 is 40 MHz, the detuning frequency is 5 MHz, and the shift data generator 44 sends, as shift data, pulse signals by means of the oscillation frequencies of $f_1 = 0$ Hz, $f_2 = 5$ MHz, $f_3 = 10$ MHz, and $f_4 = 15$ MHz. These pulse signals that serve as shift data
15 may be generated and outputted using a quartz oscillator. Regarding these pulse signals, the addition of a frequency multiplier allows other frequency pulses of 10 MHz and 15 MHz to be generated by using a frequency pulse of 5 MHz. Thus, although four type of detuning frequencies are considered in Fig. 5, shift data can be easily generated when several tens of detuning
20 frequencies are necessary.

The digital signal that indicates basic frequency f_0 that is outputted by frequency generator 22 is converted to an analog signal of basic frequency f_0 by D/A converter 21 and applied as input to quadrature modulator 43.

On the other hand, digital signals indicating any of detuning
25 frequencies f_1 to f_4 that are generated by selector/converter 41 are converted to analog signals indicating any of detuning frequencies f_1 to f_4 by

[illegible]

Quadrature modulation data for the basic frequency are thus generated in quadrature modulator 43.

Essentially, a signal that is generated at frequency data generator 22 and obtained by way of D/A converter 21 is made the carrier wave, a signal that is obtained by way of D/A converter 42 is made the frequency component that shifts a non-modulated frequency, and quadrature modulation is performed at quadrature modulator 43 to enable local oscillation signals having different frequencies to be obtained at each of the plurality of receivers 40 that are actually provided (although only one receiver 40 is shown in the figure).

There is no particular limitation on the modulation method, but in the case of $\pi/4$ shift QPSK modulation, when selector/converter 41 selects, for example, shift data f2, a shift amount of 5 MHz is generated as the generated data, and for amplitude modulation, all "0" data indicating 0 are given.

Fig. 6 is a block diagram that gives a more detailed representation of the constitution of the receiver in the working example shown in Fig. 5, and an explanation of the generation of local oscillation signal is next given with reference to Fig. 6.

20 Selector/converter 41 in Fig. 6 is provided with selection unit 51 and I/Q signal conversion unit 52; D/A converter 42 is provided with D/A conversion units 53-i and 53-q and filters 54-i and 54-q; and quadrature modulator 43 is provided with 90-degree phase shifter 55, mixers 56-i and 56-q, synthesizer 57, and filter 58.

25 Four-channel shift data that are supplied from shift data generator 44
are applied as input to selection unit 51 of selector/converter 41. Selection

unit 51 sends shift data, which are designated by frequency designation signal S that is sent in from DSP 8 shown in Fig. 4, to I/Q signal conversion unit 52.

I/Q signal conversion unit 52 converts shift data that have been received from selection unit 51 to a data format that allows processing in D/A converter 42 and quadrature modulator 43, separates the data into an I (in-phase) signal and Q (quadrature) signal, and outputs to D/A conversion units 53-i and 53-q, respectively.

The digital signals that have been received in D/A conversion units 53-i and 53-q are converted to analog signals and outputted to filters 54-i and 54-q, respectively. The clock component that is contained in the analog signal outputs of D/A conversion units 53-i and 53-q and that was used in D/A conversion units 53-i and 53-q is removed in filters 54-i and 54-q, and the resulting signals are outputted to mixers 56-i and 56-q. The clock that is used in common in the above-described receivers 40 is also used in D/A conversion units 53-i and 53-q to make the phase error identical.

Based on frequency data that have been generated in frequency data generator 22, the signal of basic frequency f_0 that is outputted as an analog signal is received as the carrier wave in quadrature modulator 43 by passing by way of D/A converter 21. The carrier wave, which is a signal of basic frequency f_0 , is applied as input to each of mixers 56-i and 56-q, but the signal that is applied to mixer 56-i passes by way of 90-degree phase shifter 55 and is received as a 90-degree phase-shifted signal, and the signal that is applied to mixer 56-q is received as a signal without phase shifting.

Using these carrier waves, the I-signal that is outputted from filter 54-i is subjected to quadrature modulation in mixer 56-i, and the Q-signal that is

outputted from filter 54-q is subjected to quadrature modulation in mixer 56-q. The outputs of each of mixers 56-i and 56-q are outputted to synthesizer 57, and the I-signal and Q-signal are synthesized by synthesizer 57.

The synthesis results in synthesizer 57 are applied as input to filter 58, which allows the passage of the synthesis result signal and all of the channel signals that are employed, and then, as a result of passage through this filter 58, the synthesis results are outputted to mixer 4 as a local oscillation signal in which unwanted radiation has been suppressed. At this time, filter 58 simultaneously removes the component of the carrier wave received from frequency data generator 22 that remained as a carrier leak.

Finally, explanation is given regarding I/Q signal conversion unit 52 that performs data conversion using a DSP.

When, for example, shift data f2 are designated by frequency designation signal S, the shift amount in I/Q signal conversion unit 52 is 5 MHz; and for amplitude modulation, data that are all "0" data that indicate 0 and that generate I/Q data that rotate the locus of an equal-amplitude circle at 5 MHz on the I-plane and Q-plane are outputted to D/A converter 42. In other words, data on the I-plane are outputted as the I-signal and data on the Q-plane are outputted as the Q-signal to D/A converter 42.

20 Quadrature modulator 43 performs quadrature modulation and synthesis of the carrier wave having a basic frequency $f_0 = 40$ MHz that is outputted by D/A converter 21 and the I/Q signal that is outputted by D/A converter 42 to generate a local oscillation signal of 45 MHz.

The various types of data for generating the carrier wave and I/Q
25 signal of detuning frequency that are used in the receivers of this working
example are created by a data generator and clock that are common to each

What is claimed is:

- 1.
2. (Deleted)
3. (Deleted)
- 5 4. (After an amendment) A local oscillation signal supply method that is used when received signals, which are received as input by way of a plurality of receivers that are each connected to respective antennas, are demodulated and outputted by a digital signal processor, characterized in that said the method comprising steps of:
 - 10 generating frequency data that contain a frequency component that is to be the local oscillation signal and sending said frequency data as a common signal source to said plurality of receivers via a signal sequence that corresponds to a wireless channel,
 - in each of said receivers, generating local oscillation signals in
 - 15 which phase and amplitude are matched in all of said receivers based on said frequency data that have been received;
 - said signal source that is supplied is a digital signal,
 - this digital signal is converted to an analog signal based on a clock signal that is common to all of said receivers to generate a local
 - 20 oscillation signal,
 - a plurality of said signal sequences are provided, each of said signal sequences having different frequency data; and
 - frequency data that are received from a plurality of said signal sequences are each subjected to quadrature amplitude modulation to
 - 25 generate a local oscillation signal having a prescribed frequency.

5. (After an amendment) A local oscillation signal supply method that is used when received signals, which are received as input by way of a plurality of receivers that are each connected to respective antennas, are demodulated and outputted by a digital signal processor, characterized in

5 that said the method comprising steps of:

generating frequency data that contain a frequency component that is to be the local oscillation signal and sending said frequency data as a common signal source to said plurality of receivers via a signal sequence that corresponds to a wireless channel,

10 in each of said receivers, generating local oscillation signals in which phase and amplitude are matched in all of said receivers based on said frequency data that have been received;

said signal source that is supplied is a digital signal,

this digital signal is converted to an analog signal based on a

15 clock signal that is common to all of said receivers to generate a local oscillation signal,

a plurality of said signal sequences supply shift data, which correspond to phase advance data for said frequency data, to all of said receivers,

20 in said receivers, shift data, in which a prescribed frequency is obtained from each of said plurality of signal sequences, are selected and subjected to signal conversion; and

shift data that have undergone selection and conversion and said frequency data are subjected to quadrature modulation to generate a

25 local oscillation signal having a prescribed frequency.

6. (Deleted)

7. (Deleted)

8. (After an amendment) A local oscillation signal supply circuit that is used when received signals, which are received as input by a plurality of receivers that are each connected to respective antennas, are demodulated and outputted by a digital signal processor, characterized in that said local oscillation signal supply circuit comprising:
- a single frequency data generator that generates frequency data that contain a frequency component that is to be a local oscillation signal and sends these data as a common signal source to said plurality of receivers by way of a single signal sequence that corresponds to a wireless channel,
- at each of the receivers, a local oscillation signal generator that generates a local oscillation signal in which phase and amplitude are matched at all of said receivers based on said frequency data that have been received from said frequency data generator,
- said frequency data generator outputs frequency data by means of digital signals,
- said local oscillation signal generator includes a digital/analog converter that converts digital signals to analog signals based on a clock signal that is common to all of said receivers; and
- said local oscillation signal generator includes a quadrature modulator that performs quadrature modulation of frequency data that are received as input from each of a plurality of said signal sequences to generate a local oscillation signal having a prescribed frequency.

9. (After an amendment) A local oscillation signal supply circuit that is used when received signals, which are received as input by a plurality of receivers that are each connected to respective antennas, are demodulated and outputted by a digital signal processor, characterized in

5 that said local oscillation signal supply circuit comprising:

a single frequency data generator that generates frequency data that contain a frequency component that is to be a local oscillation signal and sends these data as a common signal source to said plurality of receivers by way of a single signal sequence that corresponds to a wireless

10 channel,

at each of the receivers, a local oscillation signal generator that generates a local oscillation signal in which phase and amplitude are matched at all of said receivers based on said frequency data that have been received from said frequency data generator,

15 said frequency data generator outputs frequency data by means of digital signals,

said local oscillation signal generator includes a digital/analog converter that converts digital signals to analog signals based on a clock signal that is common to all of said receivers,

20 said frequency data generator is provided with a plurality of said signal sequences for outputting shift data, which correspond to phase advance data for said frequency data, to all of said receivers; and

said receivers are each provided with:

a selector/converter that selects from a signal sequence and

25 signal-converts shift data, from which a desired frequency is obtained from a plurality of said signal sequences; and

an quadrature modulator that performs quadrature modulation of shift data that have undergone selection and conversion and said frequency data to generate a local oscillation signal having a desired frequency.

Abstract of the Disclosure

With the object of accurately reproducing, by means of a small-scale circuit, differences in propagation delay at antennas of received signals that are received as input by way of each of a plurality of receivers, frequency

5 data generator 6 supplies frequency data that contain a frequency component that is synchronized in common to the plurality of receivers 2-1 to 2-n. At receiver 2-n, local oscillation signal generator 3-n, which is represented by a D/A converter, generates a local oscillation signal from frequency data that have been synchronized in common. The passing phase

10 of a received signal that is outputted from antenna 1-n by way of receiver 1-n is thus fixed, and as a result, the phase difference of received signals that are received by DSP 8 by way of the plurality of receivers 2-1 to 2-n becomes the propagation delay difference of the received signals. As frequency data, frequency data that contain a plurality of frequency components are supplied

15 to each receiver 2-n, and the frequency data are selected inside receiver 2-n and subjected to quadrature modulation to enable generation of local oscillation signals of differing intermediate frequencies.

Fig. 1

Prior Art

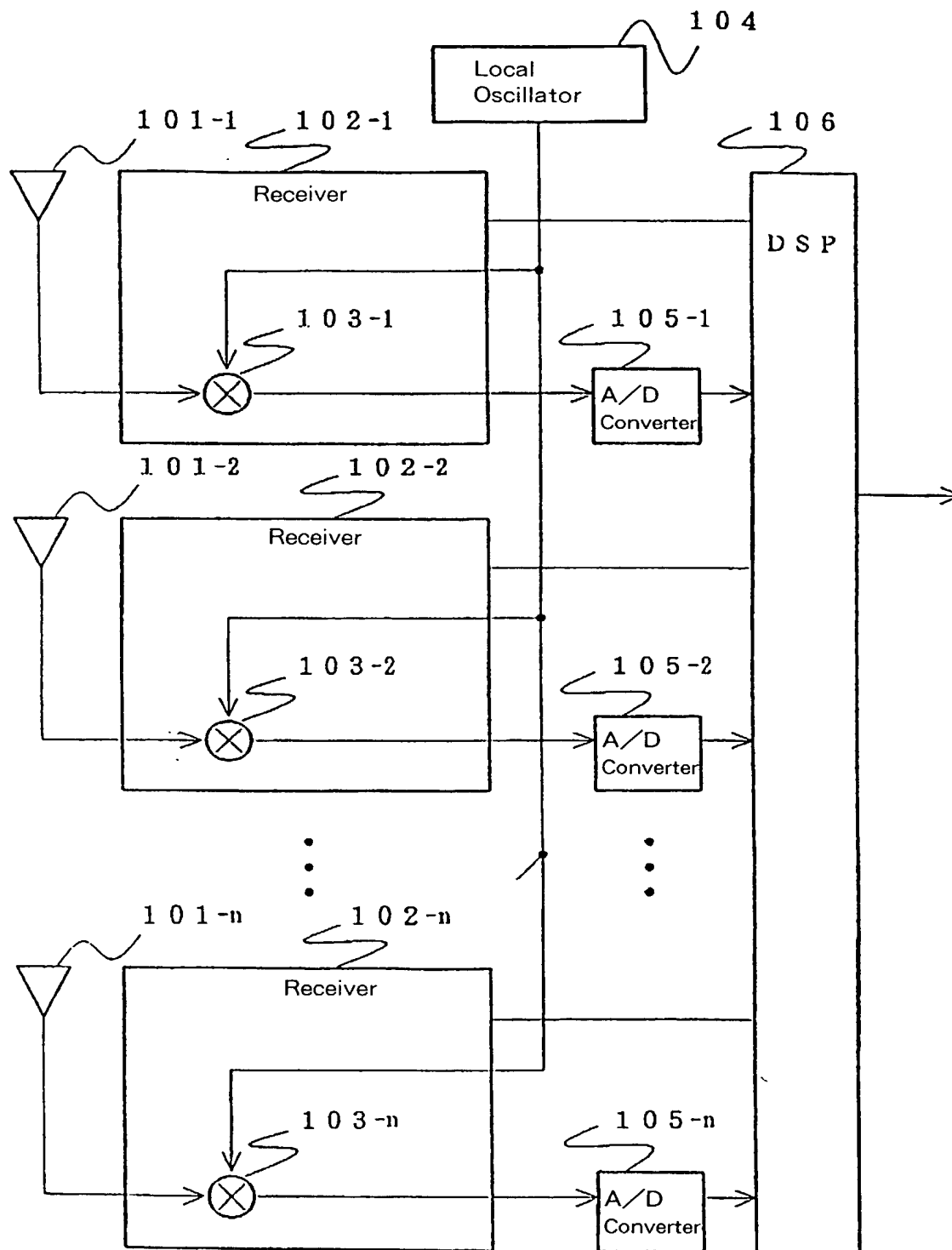


Fig. 2

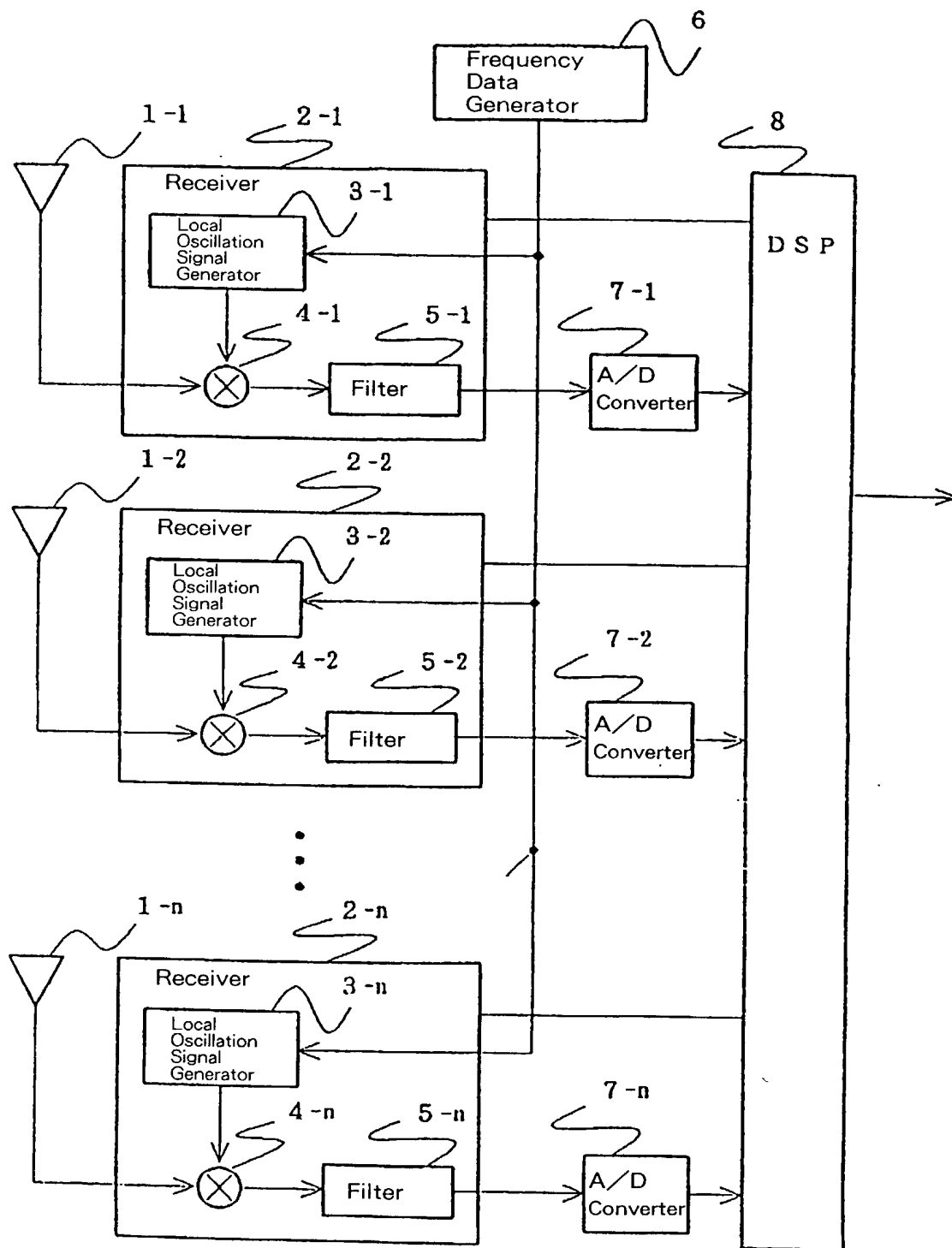


Fig. 3

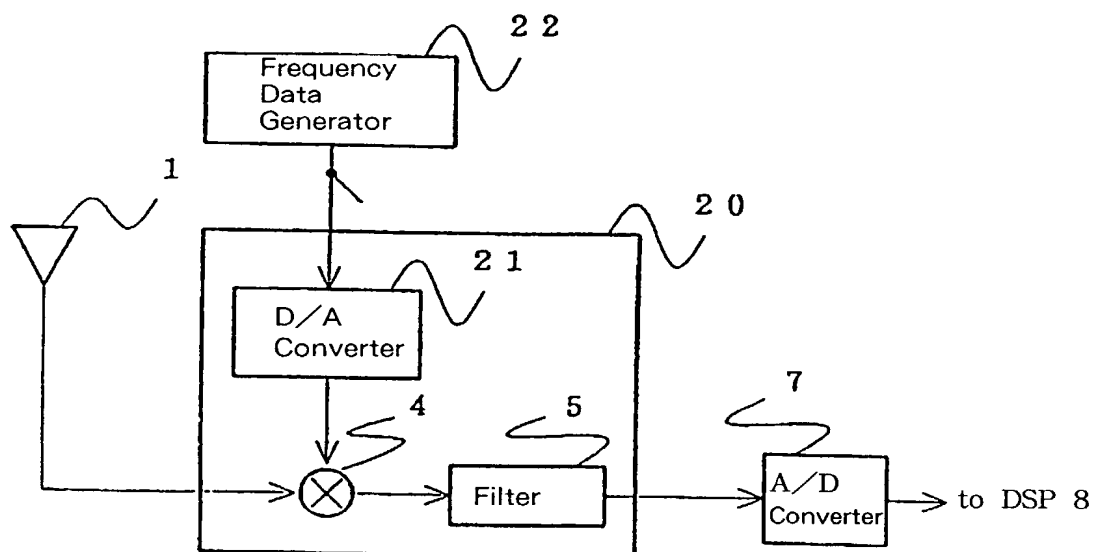


Fig. 4

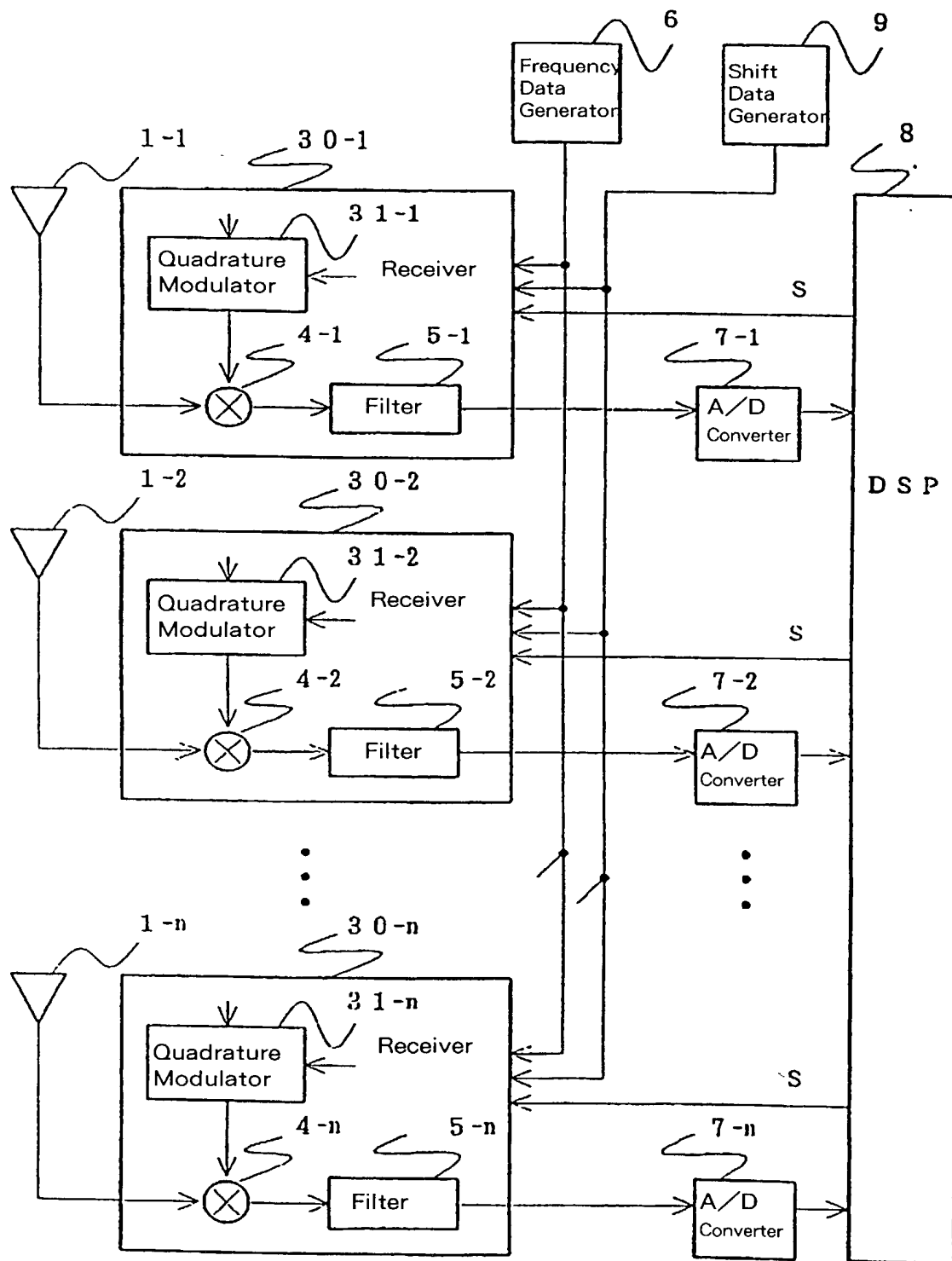
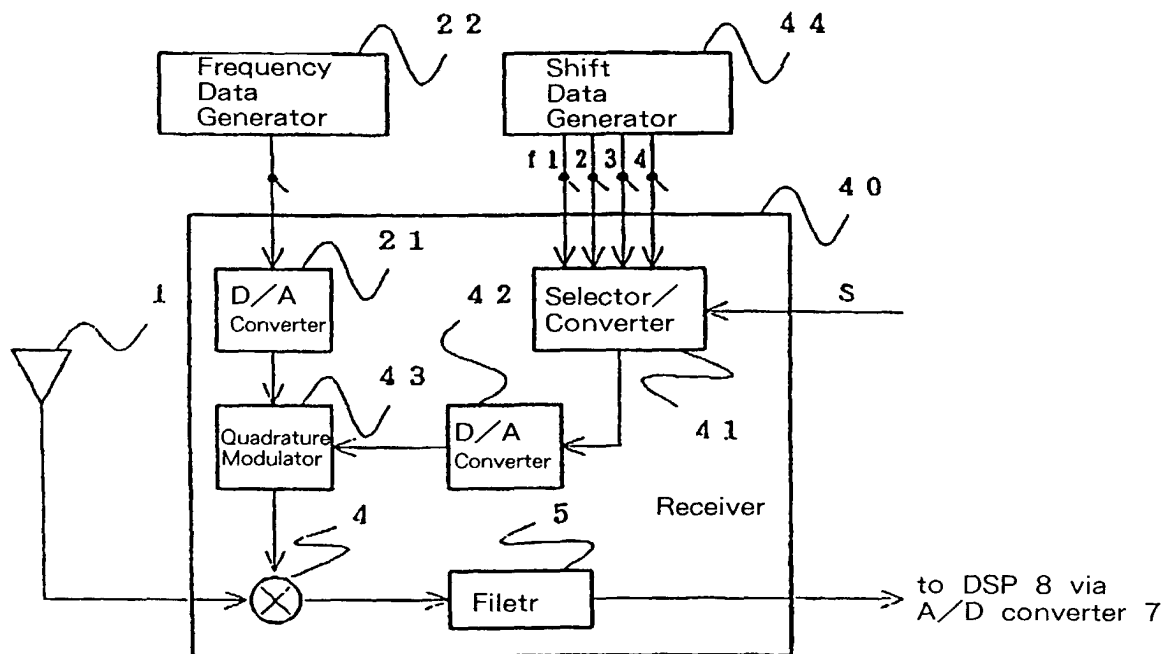


Fig. 5



Title: LOCAL OSCILLATION
SIGNAL SUPPLY METHOD AND
CIRCUIT

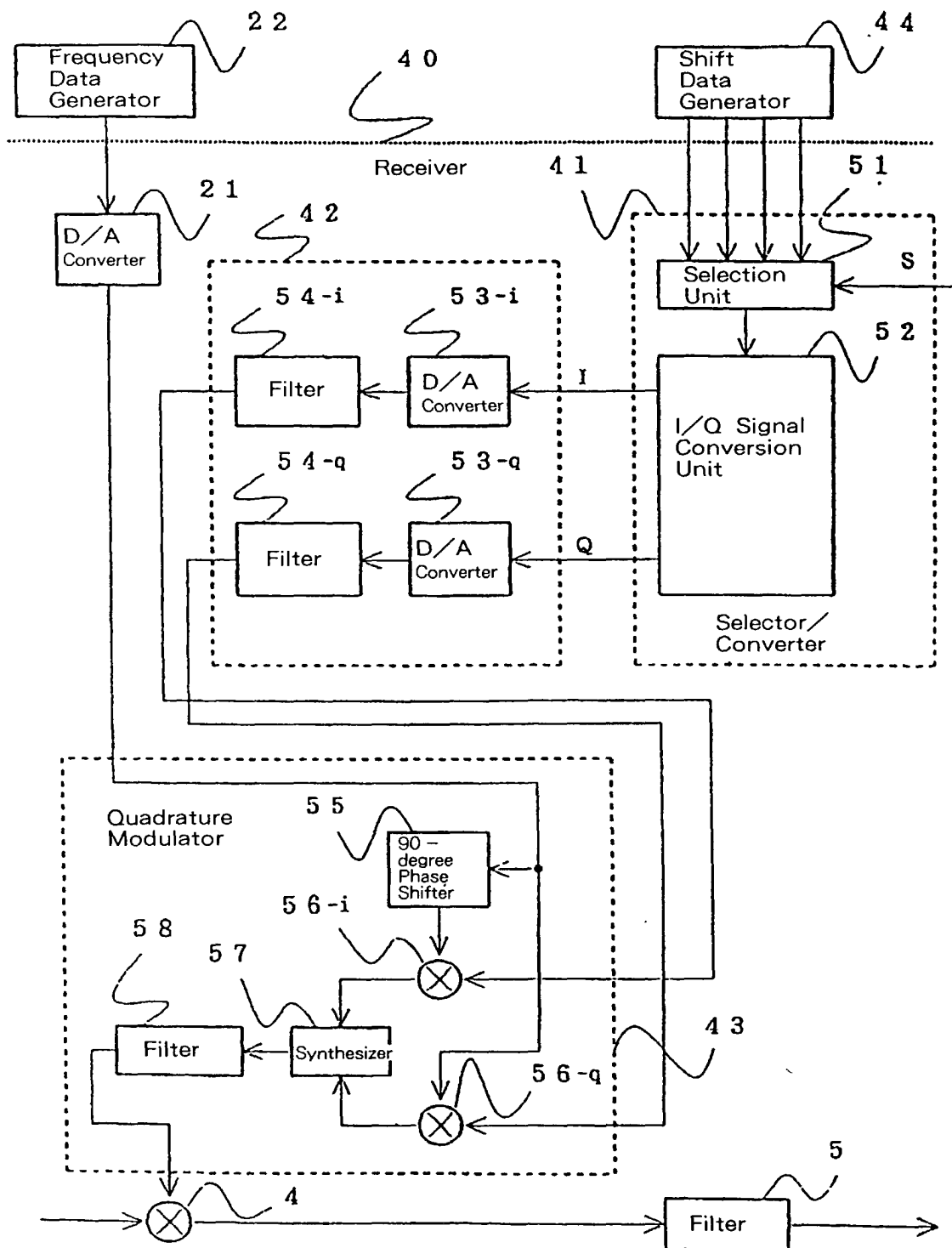
Inventor(s): Yoshitaka KAWANABE
Appl. No.: Unassigned

Fig. 6

107-088689

PCT/JP00/06494

WO 01/22605



Atty. Dkt. No. _____

DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I HEREBY DECLARE:

THAT my residence, post office address, and citizenship are as stated below next to my name;

THAT I believe I am the original, first, and sole inventor (if only one inventor is named below) or an original, first, and joint inventor (if plural inventors are named below or in an attached Declaration) of the subject matter which is claimed and for which a patent is sought on the invention entitled

LOCAL OSCILLATION SIGNAL SUPPLY METHOD AND CIRCUIT

the specification of which (check one)

 x is attached hereto.

 x was filed on Sept. 22, 2000 as United States Application Number
or PCT International Application Number PCT/JP00/06494
and was amended on _____ (if applicable).

THAT I do not know and do not believe that the same invention was ever known or used by others in the United States of America, or was patented or described in any printed publication in any country, before I (we) invented it;

THAT I do not know and do not believe that the same invention was patented or described in any printed publication in any country, or in public use or on sale in the United States of America, for more than one year prior to the filing date of this United States application;

THAT I do not know and do not believe that the same invention was first patented or made the subject of an inventor's certificate that issued in any country foreign to the United States of America before the filing date of this United States application if the foreign application was filed by me (us), or by my (our) legal representatives or assigns, more than twelve months (six months for design patents) prior to the filing date of this United States application;

THAT I have reviewed and understand the contents of the above-identified specification, including the claim(s), as amended by any amendment specifically referred to above;

THAT I believe that the above-identified specification contains a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the invention, and sets forth the best mode contemplated by me of carrying out the invention; and

THAT I acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

Atty. Dkt. No. _____

I HEREBY CLAIM foreign priority benefits under Title 35, United States Code § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below any foreign application for patent or inventor's certificate or of any PCT international application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application Number	Country	Foreign Filing Date	Priority Claimed?	Certified Copy Attached?
11-269831	Japan	Sept. 24, 1999	Yes	


I HEREBY CLAIM the benefit under Title 35, United States Code § 119(e) of any United States provisional application(s) listed below.

U.S. Provisional Application Number	Filing Date

I HEREBY CLAIM the benefit under Title 35, United States Code, § 120 of any United States application(s), or § 365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

U.S. Parent Application Number	PCT Parent Application Number	Parent Filing Date	Parent Patent Number

I HEREBY APPOINT the following registered attorneys and agents of the law firm of FOLEY & LARDNER to have full power to prosecute this application and any continuations, divisions, reissues, and reexaminations thereof, to receive the patent, and to transact all business in the United States Patent and Trademark Office connected therewith:

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I UNDERSTAND AND AGREE THAT the foregoing attorneys and agents appointed by me to prosecute this application do not personally represent me or my legal interests, but instead represent the interests of the legal owner(s) of the invention described in this application.

I FURTHER DECLARE THAT all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

1-00

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Date	March 6, 2002